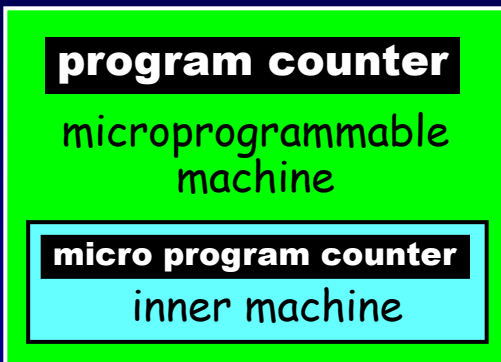




microprogrammable ¹ reconfigurable

Microprogrammable is not reconfigurable computing

Microprogrammable machine means nested instruction set machines

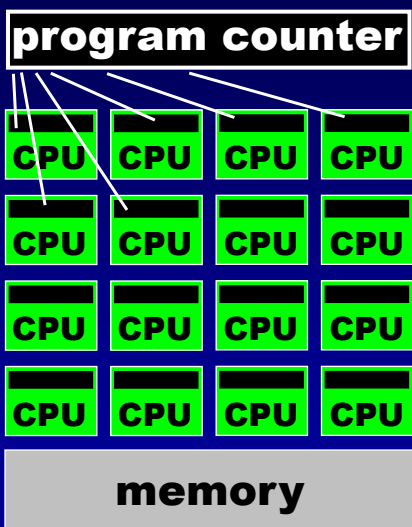


totally instruction-stream-based

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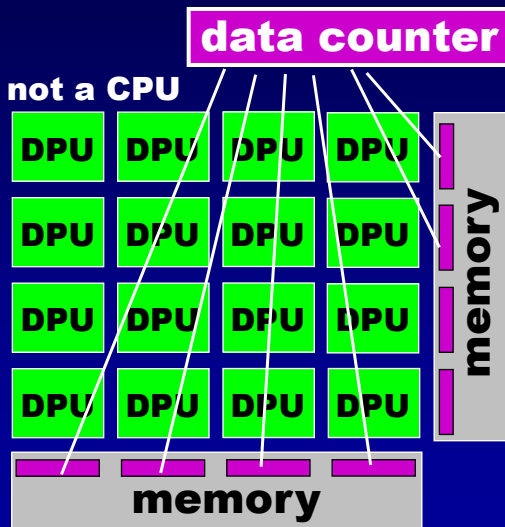
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this is not Reconfigurable Computing
it's classical parallel computing



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Confusing terminology
this is Reconfigurable Computing



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terminology clean-up

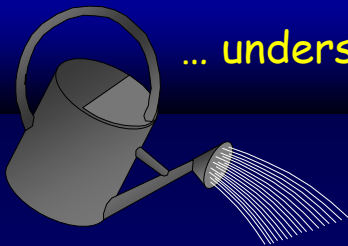
256 microprocessor CPUs
on the same microchip:

this is concurrent computing

this is not
reconfigurable computing

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... understand only this parallelism solution:

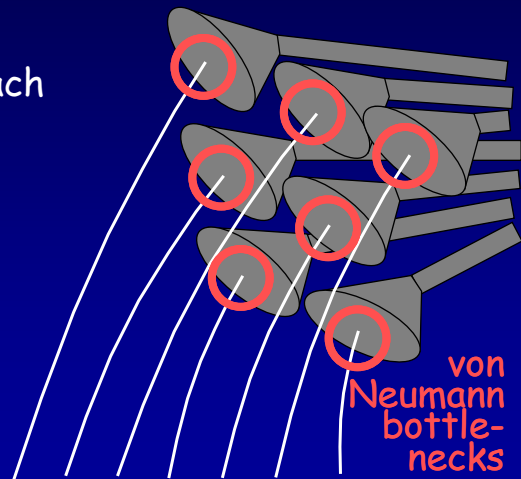
the instruction-stream-based approach

the data-stream-based approach

has no von
Neumann
bottle-
neck



ignored this
more direct one



von
Neumann
bottle-
necks

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