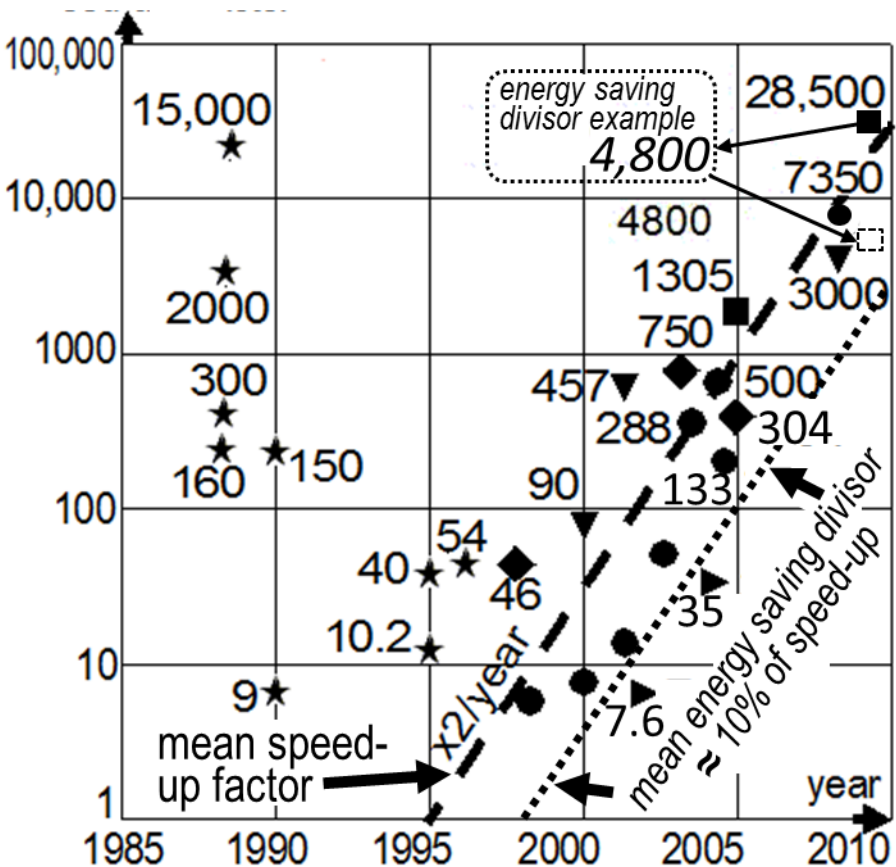


Speed-ups by Software to Configware Migrations

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- ◆ digital signal processing (FPGA)
- ▼ image / video processing (FPGA)
- cryptography / code breaking (FPGA)
- molecular biology / genome processing (FPGA)
- ▶ other applications (FPGA)
- ★ MoM xputer project & PISA (DPLA) @TU-KL

<http://www.fpl.uni-kl.de/staff/hartenstein/fig-15-speed-up.pdf>

More details: <http://www.fpl.uni-kl.de/staff/hartenstein/Hartenstein-Speedup-Factors.pdf>