

## **KARL subset used as a Design Algebra**

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**Abstract.** By means of an example this paper describes the use of the hardware description language KARL to develop the concise specification of design problems such as generating the input to register transfer level simulation, of, to routing problems.

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[http://www.fpl.uni-kl.de/staff/hartenstein/Design\\_Algebra.pdf](http://www.fpl.uni-kl.de/staff/hartenstein/Design_Algebra.pdf)